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Date	Total pages	Attorney number	Client/matter number
November 12, 2009		10569	

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Application Serial No.: 08/449,097
Attorney Docket No.: PMC-003 C48

Examiner Michael J. Moore, Jr.

DRAFT PROPOSED AMENDMENT

1. (Cancelled)

2. (Previously presented) A method of processing signals at a receiver station, said receiver station having a plurality of processors, said method comprising the steps of:

receiving an information transmission including a digital television signal and a message stream;

detecting said message stream in said information transmission;

selecting at least one message of said detected message stream;

inputting at least a first portion of said selected at least one message to a control processor;

selecting control information in said inputted first portion of said selected at least one message;

selecting and computing under the control of said control processor, other portions of said message stream in said plurality of processors, based on said control information;

processing said selected other portions of said message stream simultaneously at said plurality of processors;

controlling the timing of communicating television programming in accordance with said message stream; and

storing information evidencing the availability, use or usage of said television programming or said message stream.

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3. (Previously presented) A method of processing signals at a receiver station, said receiver station having a plurality of processors, said plurality of processors including a first control processor which controls a remainder of said plurality of processors based on a message stream, said method comprising the steps of:

receiving an information transmission including a message stream at a transmission station;

generating a control portion of said message stream at said transmission station that is effective at a receiver station to cause said first control processor to select portions of said message stream that control said control processor and said remainder of said plurality of processors to perform different functions comprising (i) processing television programming and (ii) controlling the timing of communicating said television programming; and

transmitting said message stream to be received at said receiver station.

4. (Currently amended) A method of processing signals in a network, comprising the steps of:

receiving an information transmission to be transmitted;

receiving an instruct signal which is effective ~~to one of:~~

~~(a)~~ effect at a transmitter station to generate at least a first message that is effective to enable a receiver station to control the reception or presentation of television programming, select and output portions of said information transmission to a plurality of processors at said receiver station based on control information in said first message, process said portions of said information transmission simultaneously at said plurality of processors and meter or monitor the availability, use or usage of said television programming or said at least a first message; ~~and~~

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~~(b) effect a first receiver station to generate at least a first message that is effective to enable a second receiver station to control the reception or presentation of television programming and meter or monitor the availability, use or usage of said television programming as said at least a first message;~~

receiving a transmitter control signal which operates at ~~one of~~ said transmitter station ~~and said first receiver station~~ to communicate said at least a first message to a transmitter; and

transmitting said information transmission, ~~and~~ said ~~control signal and said transmitter control signal~~ first message.

5. (Previously presented) The method of claim 2, further comprising the step of programming said control processor to execute a controlled function in response to said at least one message.

6. (Currently amended) The method of claim 5, further comprising the step of programming said control processor to compare information stored in at least a first ~~of said at least one~~ register memory with control function invoking information.

7. (Currently amended) The method of claim 6, further comprising the step of programming said control processor to compare information stored in at least a second ~~of said at least one~~ register memory with information that identifies a length or format of at least a portion of said at least one message.

8. (Currently amended) The method of claim 2, wherein said ~~at least one~~ a register memory includes an input signal register memory and said step of selecting control information in said

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~~inputted first portion of said selected at least one message and communicating said selected control information to a plurality of register memories comprises~~ further comprising the steps of

communicating said at least a first portion of said selected at least one message to said input signal register memory;

selecting information at said input signal register memory to compare or communicate; and communicating said control information to at least a second of said at least one register memory.

9. (Previously presented) The method of claim 8, further comprising the step of communicating at least one of said other portions of said message stream to said input signal register memory.

10. (Previously presented) The method of claim 2, further comprising the step of controlling a switch to output at least one of said selected other portions of said message stream to a specific one of said plurality of processors.

11. (Previously presented) The method of claim 10, further comprising the step of controlling said switch to communicate said at least one of said selected other portions of said message stream from at least one of (1) said control processor and (2) a buffer that inputs to said control processor.

12. (Previously presented) The method of claim 10, wherein said switch outputs said at least one of said selected other portions to said control processor.

13. (Previously presented) The method of claim 10, wherein said switch outputs said at least one of said selected other portions to at least one of a signal processor and a central processor.

14. (Previously presented) The method of claim 10, further comprising the step of programming said control processor to control said switch based on information included in said message stream.

15. (Currently amended) The method of claim 14, further comprising the steps of:

programming said control processor with comparison inform information to serve as a basis for determining the length or format of said at least one segment message of said message stream; and

programming said control processor to compare information stored at said at least one register memory to said comparison information.

16. (Previously presented) The method of claim 14, wherein said control processor and said switch are located on a single microchip.

17. (Previously presented) The method of claim 2, wherein said control processor receives said at least a first portion of said message from a first of said plurality of processors and controls outputting to a second of said plurality of processors.

18. (Currently amended) The method of claim 17, wherein said first control processor performs at least one of (1) converting information detected in said message stream based on protocols and (2) assembling processor code based on information detected in said message stream.

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said ~~message~~ method further comprising the step of communicating machine language code to said second processor to said selected other portions of said message stream.

19. (Previously presented) The method of claim 2, wherein a decryptor decrypts at least some of said message stream, said method further comprising the step of outputting one or more of said selected other portions of said message stream to said decryptor.

20. (Previously presented) The method of claim 19, further comprising the steps of
selecting at least a portion of said message stream; and
controlling said decryptor in accordance with said selected at least a portion of said message stream.

21. (Previously presented) The method of claim 20, wherein said selected at least a portion of said message stream comprises a decryption key.

22. (Previously presented) The method of claim 21, further comprising the step of decrypting at least some of said digital television signal in accordance with said decryption key.

23. (Currently amended) The method of claim 19, further comprising the steps of
storing a decrypted portion of said at least some of said message stream at ~~some or all of said~~ ~~at least one~~ a register memory; and
processing decrypted portions of said message stream simultaneously.

24. (Previously presented) The method of claim 23, further comprising the step of decrypting processor code included in said message stream.

25. (Previously presented) The method of claim 2, wherein a standard informs said receiver station of a signal to be processed, said method further comprising the step of evaluating at least some of said selected at least one message based on said standard.

26. (Currently amended) The method of claim 25, ~~wherein~~ further comprising the step steps of:

storing at least a portion of said standard at one or more of a Standard Word and a Standard Length memory; and

programming said receiver station to compare data received in said information transmission to information included at said one or more of a Standard Word and a Standard Length memory.

27. (Currently amended) The method of claim 25, wherein said receiver station identifies, based on said standard, one of (1) an end of a prior message and (2) a header in said selected at least one message.

28. (Previously presented) The method of claim 25, further comprising the step of causing said control processor to process an interrupt signal based on said standard.

29. (Previously presented) The method of claim 2, further comprising the step of programming said receiver station to communicate a processor interrupt signal to at least one of said plurality of processors.

30. (Previously presented) The method of claim 29, wherein two or more of said plurality of processors are adapted to communicate or respond to processor interrupt signals, said method further comprising the step of programming said receiver station to select at least one of said two or more processors to interrupt.

31. (Cancelled)

32. (Previously presented) The method of claim 2, wherein said receiver station includes a video monitor and a first of said plurality of processors generates a video signal to be displayed as part of said television programming, said method further comprising the step of outputting to said first processor a first of said selected other portions of said message stream which causes said first processor to communicate said video signal to said video monitor.

33. (Currently amended) The method of claim 32, wherein said receiver station includes a speaker and a second of said plurality of processors generates an audio signal including audio to be emitted as part of said television programming, said method further comprising the step of outputting to said second processor a second of said selected other portions of said message stream which causes which causes said second processor to communicate said audio signal to said speaker.

34. (Previously presented) The method of claim 33, wherein said receiver station includes one or more of a tuner and a portion receiver and a third of said plurality of processors is adapted to control said one or more of a tuner and a portion receiver, said method further comprising the step of

programming said third processor to control said one or more of a buffer and a portion received based on information included in said selected other portion of said message stream.

33. (Previously amended) The method of claim 2, wherein at least one of said selected other portions of said message stream includes first processor code that controls at least one of said plurality of processors to generate information content of one or more video or audio signals, said method further comprising the steps of:

selecting second processor code included in said selected at least one message; and

communicating said first processor code in accordance with said second processor code.

35. (Currently amended) The method of claim 35, wherein said second processor code programs said control processor to select control information in said message stream and communicate said selected control information to said at least one a register memory, said method further comprising the step of processing control information of a new composition and/or length in accordance with said second processor code.

37 to 40. (Cancelled)

41. (Previously presented) The method of claim 2, further comprising the steps of:
generating a first instruction specifying a control function to be executed;
generating a second instruction specifying a data structure, length, or format;
organizing said first and second instructions in a sequence, said sequence comprising a command; and

communicating to a signal generator cadence information which operates at said receiver station to select a portion of at least one message of said message stream.

42. (Previously presented) The method of claim 41, further comprising the steps of: processing data specifying a condition which must exist at said receiver station, and including said data specifying a condition in said contract.

43 to 46. (Cancelled)

47. (Currently amended) The method of claim 41, further comprising the step of transmitting an instruction which operates at said receiver station to control at least one of said plurality of processors processors to select a second processor to be interrupted.

48. (Unchanged) The method of claim 3, further comprising the steps of:
selecting at least some of said television programming at said transmission station;
selecting meter-monitor data; and
organizing said message stream to include said selected at least some of said television programming and said selected meter-monitor data.

49 to 55. (Cancelled)

56. (Previously presented) A method of processing signals in a television receiver, said television receiver having a plurality of processors, said method comprising the steps of:

receiving an information transmission including digital television signals and a message stream;

detecting said message stream in said information transmission;

inputting at least a first portion of said message stream to a control processor;

selecting control information in said at least a first portion of said message stream and communicating said selected control information to at least one register memory;

comparing stored function invoking data to the contents of said at least one register memory;

inputting said digital television signals to said plurality of processors on the basis of one or more matches;

processing of said digital television signals simultaneously at two or more of said plurality of processors; and

displaying television programming included in said digital television signals.

57. (Currently Amended) A method of television or video signal processing at a television or video receiver, said television or video receiver having a plurality of processors, comprising the steps of:

(1) receiving an information transmission, said information transmission including a message stream;

(2) receiving a control signal which operates at a transmitter station to communicate said information transmission to a transmitter; and

(3) transmitting said message stream, said message stream enabling said receiver station to select control information in said message stream, compare said control information to a stored function invoking datum, input selected digital television or digital video signals to said plurality of processors on the basis of one or more matches of said control information to said stored function,

invoking datum, simultaneously process said selected digital television or digital video signals at two or more of said plurality of processors, and display television programming or video information included in said selected digital television or digital video signals on the basis of said comparison of said control information to said stored function invoking datum.

58. (Currently amended) A method of television or video signal processing at a television or video receiver, said television or video receiver having a plurality of processors, comprising the steps of:

(1) receiving an information transmission including a message stream; and

(2) causing said message stream to be communicated to a transmitter at a specific time, thereby to transmit said message stream, said message stream enabling said receiver station to select control information in said message stream, compare said control information to a stored function invoking datum, input selected digital television or digital video signals to said plurality of processors on the basis of one more matches of said control information to said stored function invoking datum, simultaneously process said selected digital television or digital video signals at two or more of said plurality of processors, and display television programming or video information included in selected digital television or digital video signals on the basis of said comparison of said control information to said stored function invoking datum.

59. (Previously presented) The method of claim 36, wherein said information transmission is processed using forward error correction techniques.

60. (Previously presented) The method of claim 56, further comprising the step of programming said control processor to execute a controlled function in response to said message stream.

61. (Previously presented) The method of claim 56, further comprising the step of programming said control processor to compare information stored at said at least one register memory to said stored function invoking data.

62. (Previously presented) The method of claim 56, further comprising the step of programming said control processor to compare information stored at said at least one register memory with information that specifies a composition of said message stream.

63. (Previously presented) The method of claim 56 wherein said step of selecting control information in said at least a first portion of said message stream and communicating said selected control information to at least one register memory includes:

communicating said control information to a first part of said at least one register memory; and

communicating said control information to a second part of said at least one register memory.

64. (Previously presented) The method of claim 56, further comprising the step of controlling a digital switch on the basis of said one or more matches.

65. (Currently amended) The method of claim 56, wherein said a digital switch communicates at least some of said digital television signals in said plurality of processors, said

method further comprising the step of communicating said at least some of said message-stream digital television signals from said at least one register memory to at least one of said digital switch and a second one of said plurality of processors.

66. (Previously presented) The method of claim 58, wherein said control processor receives said at least a first portion of said message stream from a first of said plurality of processors, said method further comprising the step of communicating at least a second portion of said message stream to a second of said plurality of processors.

67. (Previously presented) A method of processing signals at a receiver station, said receiver station having a video monitor and a plurality of processors, said method comprising the steps of:

receiving an information transmission including digital video signals and control information;

detecting said control information in said information transmission and passing said control information to a control processor;

communicating said control information selectively to at least one register memory;

comparing stored function invoking data to the contents of said at least one register memory;

communicating said digital video signals to at least one of said plurality of processors on the basis of one or more matches;

processing said digital video signals simultaneously at two or more of said plurality of processors; and

displaying video included in said digital video signals.

68. (Previously presented) The method of claim 67, wherein said video includes television programming.

69. (Previously presented) The method of claim 67, wherein said control information is detected in a message stream, said method further comprising the step of communicating at least some of said message stream from a first of said plurality of processors.

70. (Previously presented) The method of claim 67, wherein said control processor receives said control information from a first of said plurality of processors, said method further comprising the step of communicating said control information to a second of said plurality of processors.

71. (Currently amended) The method of claim 70, wherein said first processor performs one of (1) converting information detected in said ~~message stream~~ information transmission based on protocols and (2) assembling processor code based on data detected in at least a first portion of said information transmission, said ~~message~~ method further comprising the step of communicating machine language code to said second processor based on data detected in at least a second portion of said information transmission.

72. (Previously presented) A method of processing signals in a television receiver, said television receiver having a plurality of processors, said method comprising the steps of:

receiving an information transmission including digital television signals and cadence information;

detecting and passing said cadence information to a control processor;

communicating said cadence information selectively to at least one register memory;

comparing stored communication control information to the contents of said at least one register memory;

communicating said digital television signals to said plurality of processors on the basis of one or more matches;

processing said digital television signals simultaneously at two or more of said plurality of processors; and

displaying television programming included in said digital television signals.

73. (Currently amended) The method of claim 72, wherein said control processor controls a decryptor to decrypt at least some of said digital television signals, said method further comprising the step of controlling a digital switch to communicate said at least some of said digital television signals to or from said decryptor in accordance with said cadence information.

74. (Previously presented) The method of claim 73, wherein said cadence information is unencrypted.

75. (Currently amended) The method of claim 73, wherein said cadence information is detected in a message stream, said method further comprising the steps of:

storing a decrypted portion of said at least some of said message stream at said at least one register memory; and

processing decrypted portions of said message stream simultaneously.

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76. (Previously presented) The method of claim 72, wherein a standard identifies a signal to be processed, said method further comprising the step of identifying a start of one or more of (1) said digital television signals and (2) said cadence information based on said standard.

77. (Previously presented) The method of claim 76, further comprising the steps of:

programming said television receiver to compare the contents of a first part of said at least one register memory to a second part of said at least one register memory; and

storing said standard at said at least one register memory.

78. (Previously presented) The method of claim 76, wherein said receiver identifies, based on said standard, one of a start and an end of a message.

79. (Previously presented) The method of claim 72, further comprising the step of causing said control processor to process an interrupt signal based on said cadence information.

80. (Previously presented) The method of claim 56, wherein said television receiver includes a video monitor and information included in said message stream controls a first of said plurality of processors to generate video to be displayed as part of said television programming, said method further comprising the step of communicating to said first processor a first instruction which causes said first processor to communicate said video to said video monitor.

81. (Previously presented) The method of claim 80, wherein said television receiver includes a speaker and information included in said message stream controls a second of said plurality of processors to generate an audio signal including audio to be emitted as part of said television

programming, said method further comprising the step of communicating to said second processor a second instruction which causes said second processor to communicate said audio signal to said speaker.

82. (Previously presented) The method of claim 86, wherein said first of said plurality of processors generates said video in accordance with information included in said message stream, said method further comprising the step of communicating at least some of said message stream to said plurality of processors.

83. (Previously presented) The method of claim 86, wherein at least some of said digital television signals are included in said message stream.

84. (Previously presented) The method of claim 59, wherein said message stream includes first processor code which controls said plurality of processors to generate information content of a video or audio signal, said method further comprising the steps of:

    inputting to said control processor second processor code detected in said message stream; and

    communicating said first processor code in accordance with said second processor code.

85. (Previously presented) The method of claim 84, wherein said second processor code programs said control processor to select or respond to said content information.

86. (Currently amended) The method of claim 59, further comprising the steps of:

receiving said information transmission at a signal generator operatively connected to said transmitter;

generating first cadence information which is effective at said receiver station to execute a predetermined instruction and at least one message element including one or more instructions to be directed to a specific one of said plurality of processors; and

embedding said cadence information and said at least one message element in said information transmission before communicating said information transmission to said transmitter.

87. (Currently amended) The method of claim 86, wherein said specific processor includes at least one register memory, said method further comprising the steps step of: communicating to said signal generator second cadence information which operates at said at least one register memory to select or identify said control information.

88. (Previously presented) The method of claim 87, further comprising the steps of:
generating a first instruction specifying a control function to be executed;
generating a second instruction specifying a data structure, length, or format;
organizing said first and second instructions in a sequence, said sequence comprising a command; and
embedding said command in said message stream.

89. (Previously presented) The method of claim 88, further comprising the steps of:
processing data specifying a condition which must exist at said receiver; and
including said data specifying a condition in said command.

99 to 95. (Cancelled)

96. (Previously presented) The method of claim 57, further comprising the steps of:

selecting television programming;

selecting meter-monitor data; and

embedding said selected television programming and said selected meter-monitor data in said

message stream.

97. (Previously presented) The method of claim 57, wherein information included in said

message stream enables said receiver, or apparatus operatively connected to said receiver, to select

said selected digital television or digital video signals, said method further comprising the step of

transmitting said digital television or digital video signals.

98 to 105. (Cancelled)

106. (Previously presented) A method of processing signals at a receiver station, said

receiver station having a plurality of processors, said method comprising the steps of:

receiving a broadcast or cablecast information transmission including at least one of a digital

video and a digital audio signal;

detecting a message stream in said broadcast or cablecast information transmission;

selecting a message communicated in said detected message stream;

inputting at least a portion of said selected message to a control processor;

communicating a predetermined datum to a flag memory;

outputting selected portions of said message to said plurality of processors;

processing said selected portions simultaneously;

selecting a processor from said plurality of processors to interrupt on the basis of control information included in said message;

communicating a processor interrupt to said selected processor; and

controlling apparatus presenting media programming based on the content of said flag memory.

107. (Currently amended) A method of processing signals at a receiver station, said receiver station having a plurality of processors, said method comprising the steps of:

(a) receiving a broadcast or cablecast information transmission at a transmission station;

(b) generating a message that is effective to enable said receiver station to output selected portions of said message to said plurality of processors, to process said selected portions simultaneously, to select a processor from said plurality of processors to interrupt on the basis of control information included in said message, (c) at least one of communicating and responding to communicating a processor interrupt on the basis of the content of a flag memory (d) controlling by said selected processor and to control apparatus presenting media programming based on said processor interrupt the content of a flag memory; and

(e) transmitting said message.

108. (Currently amended) A method of processing signals in a network, said method comprising the steps of:

(a) receiving a broadcast or cablecast information transmission;

(b) receiving an instruct signal which performs at least one of:

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(i) effecting effective at a transmitter station to generate a first message of a message stream that is effective to enable a remote receiver station to: (1) output selected portions of said message stream to a plurality of processors, (2) process said selected portions simultaneously, (3) select a processor from a said plurality of processors to interrupt on the basis of control information included in said first message, (4) (4) at least one of communicate and respond to a processor interrupt on the basis of the content of a flag memory, and (5) (5) control apparatus presenting media programming based on said processor interrupt; and

(3) effecting a receiver station to generate a second message that is effective to enable a remote receiver station to: (1) select a processor from a plurality of processors to interrupt on the basis of control information included in said second message, (2) at least one of communicate and respond to a processor interrupt on the basis of the content of a flag memory, and (3) control apparatus presenting media programming;

(c) receiving a transmitter control signal which operates at said transmitter station to communicate at least one of said interrupt signal and said first message to a transmitter; and

(d) transmitting said information transmission, said transmitter control signal, and said at least one of said interrupt signal and said first message.

109. (Previously presented) A method of processing signals in a television receiver, said television receiver having a plurality of processors, said method comprising the steps of:

receiving an information transmission including a digital television signal and a message stream;

detecting said message stream in said information transmission;

selecting at least one message of said detected message stream;

inputting at least a first portion of said selected at least one message to a control processor;

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selecting control information in said inputted first portion of said selected at least one
message and communicating said selected control information to a plurality of dedicated register
memories;

controlling a digital switch on the basis of a plurality of comparisons at said memories,
outputting selected other portions of said message stream to said plurality of processors;
processing said selected other portions of said message stream simultaneously; and
controlling the reception or presentation of television programming in accordance with said
selected other portions.

110. (Previously presented) The method of claim 109, further comprising the step of
programming said control processor to execute a controlled function in response to an execution
instruction included in said at least one message.

111. (Previously presented) The method of claim 110, further comprising the step of
programming said control processor to compare information stored at some or all of a first of said
plurality of dedicated register memories with control function invoking information.

112. (Previously presented) The method of claim 111, further comprising the step of
programming said control processor to compare information stored at some or all of a second of said
plurality of dedicated register memories with information that identifies the composition of said at
least one message.

113. (Previously presented) The method of claim 109, wherein said plurality of dedicated
register memories include an input signal register memory and said step of selecting control

information in said inputted first portion of said selected at least one message and communicating said selected control information to a plurality of dedicated registers memories comprises

communicating said at least a first portion of said selected at least one message to said input signal register memory;

selecting information in said input signal memory to compare or communicate; and

communicating said control information to a second of said plurality of dedicated register memories.

113. (Currently amended) The method of claim 109, further comprising the step of communicating at least one of said other portions of said message stream to said an input signal register memory included in said plurality of dedicated register memories.

115. (Currently amended) The method of claim 114, further comprising the step of communicating said at least one of said other portions of said message stream from said input signal register memory to at least one of said digital switch and a second one of said plurality of processors.

116. (Currently amended) The method of claim 109, wherein said control processor receives said at least a first portion of said message from a first of said plurality of processors, said method further comprising the step of controlling said digital switch to communicate one or more other portions of said message stream to a second one of said plurality of processors.

117. (Cancelled)

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118. (Currently amended) The method of claim 109, wherein said control processes controls a decryptor to decrypt at least some of said at least one message, said method further comprising the step of controlling said digital switch to communicate said at least some of said at least one message to or from said decryptor.

119. (Currently amended) The method of claim 118, further comprising the steps of:
storing a decrypted portion of said at least some of said at least one message at some of said plurality of dedicated register memories; and
processing decrypted portions of said message stream simultaneously.

120. (Previously presented) The method of claim 109, wherein a standard identifies a signal to be processed, said method further comprising the step of identifying the start of said selected at least one message based on said standard.

121. (Previously presented) The method of claim 120, further comprising the step of:
programming said television receiver to compare the contents of at least a first of said plurality of dedicated register memories to a second of said plurality of dedicated register memories; and
storing said standard at at least one of said plurality of dedicated register memories.

122. (Currently amended) The method of claim 120, wherein said receiver identifies, based on said standard, at least one of (1) an end of a prior message and (2) a header in said selected at least one message.

123. (Previously presented) The method of claim 120, further comprising the step of causing said control processor to process an interrupt signal based on said identified signal to be processed.

124. (Previously presented) The method of claim 109, wherein said television receiver includes a video monitor and information included in said message stream controls a first of said plurality of processors to generates a video signal to be displayed as part of said television programming, said method further comprising the step of causing at least one of said control processor and said digital switch to communicate to said first processor a first signal which causes said first processor to communicate said video signal to said video monitor.

125. (Currently amended) The method of claim 124, wherein said television receiver includes a speaker and information included in said message stream controls a second of said plurality of processors to ~~generates~~ generate an audio signal including audio to be emitted as part of said television programming, said method further comprising the step of causing at least one of said control processor and said digital switch to communicate to said second processor a second signal which causes said second processor to communicate said audio signal to said speaker.

126. (Previously presented) The method of claim 124, wherein said first of said plurality of processors generates said video signal in accordance with at least one of said selected other portions of said message stream, said method further comprising the step of controlling said digital switch to communicate said at least one of said selected other portions of said message stream to said first of said plurality of processors.

127. (Currently amended) The method of claim 109 wherein said at least one of said other portions of said message stream includes first processor code addressed to a processor that generates information content of a video or audio signal, said method further comprising the steps of:

inputting to said control processor second processor code received in said selected at least one message; and

communicating, in accordance with said second processor code, said at least one of said other portions of said message stream to said processor that generates said information content.

128. (Previously presented) The method of claim 127, wherein said second processor code programs said control processor to select control information in said message stream and communicate said selected control information to said plurality of dedicated register memories; said method further comprising the step of processing control information of a new composition and/or length in accordance with said second processor code.

129. (Currently amended) A method of video signal processing at a video receiver, said video receiver having a plurality of processors, said method comprising the steps of:

receiving an information transmission including a digital video signal and a message stream;

detecting said message stream in said information transmission;

selecting at least one message of said detected message stream;

inputting at least a portion of said selected at least one message to a control processor;

selecting control information in said inputted portion of said selected at least one message and communicating said selected control information to at least one dedicated register memory;

controlling a digital switch on the basis of a plurality of comparisons of said at least one dedicated register memory;

outputting selected portions of said message stream to said plurality of processors,

processing said selected portions of said message stream simultaneously; and

controlling the reception or presentation of video in accordance with said at least one

message.

130. (Previously presented) The method of claim 129, wherein said information transmission

is processed using forward error correction techniques.

131. (Currently amended) A method for an origination station or intermediate transmission

station to control television or video signal processing at a television or video receiver, said

television or video receiver having a plurality of processors, said method comprising the steps of:

(A) receiving an information transmission, said information transmission including a message

stream;

(B) receiving a control signal which operates at a transmitter station to communicate said

information transmission to a transmitter; and

(C) transmitting said message stream, said message stream to effect said receiver station to

select a portion of at least one message from said message stream and control the reception or

presentation of television programming or video information in accordance with said at least one

message by selecting and outputting portions of said information transmission to said plurality of

processors based on control information in said at least one message and processing said portions of

said information transmission simultaneously at said plurality of processors

132. (Currently amended) The method of claim 131, wherein said step of receiving an information transmission is at a signal generator operatively connected to said transmitter, and further comprising the steps of:

generating first cadence information which is effective at said receiver station to execute a predetermined instruction and at least one message element including one or more instructions to be directed to a specific at least one of said plurality of processors, and

embedding said cadence information and said at least one message element in said information transmission before communicating said information transmission to said transmitter.

133. (Currently amended) The method of claim 132, wherein said specific processor includes a plurality of dedicated register memories, said method further comprising the step of:

communicating to said signal generator second cadence information which operates at said a plurality of dedicated register memories to select said portion of at least one message.

134. (Currently amended) A method for an origination station or intermediate transmission station to control processing of signals in a television or video receiver, said television or video receiver having a plurality of processors, said method comprising the steps of:

(1) receiving an information transmission, including a message stream; and

(2) causing a portion of said message stream to be communicated to a transmitter at a specific time, thereby to transmit said portion of said message stream, said portion of said message stream to effect said receiver station to control the reception or presentation of some television programming or video information in accordance with said message portion by selecting and outputting portions of said information transmission to said plurality of processors based on content information in said

portion of said message stream and processing said portion of said information transmission

simultaneously at said plurality of processors.

133. (Cancelled)